

SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURING THE SAME

Publication number: JP2004140274 (A)

Publication date: 2004-05-13

Inventor(s): TAKEHIRO SHINOBU +

Applicant(s): OKI ELECTRIC IND CO LTD +

Classification:

- international: H01L21/28; H01L21/336; H01L21/76; H01L21/762; H01L21/768; H01L27/12; H01L29/417; H01L29/786; H01L21/02; H01L21/70; H01L27/12; H01L29/40; H01L29/66; (IPC1-7): H01L21/28; H01L21/76; H01L21/762; H01L21/768; H01L29/417; H01L29/786

- European: H01L21/336D2B; H01L29/786A

Application number: JP20020305349 20021021

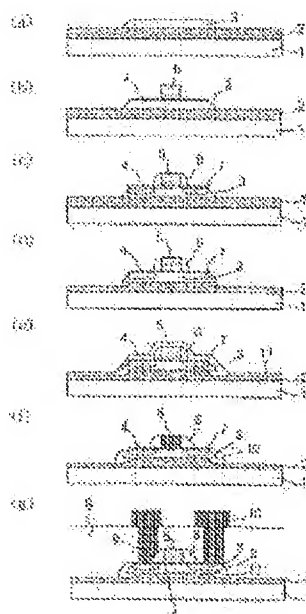
Priority number(s): JP20020305349 20021021

Also published as:

JP3532188 (B1)
US2004075139 (A1)
US6638733 (B2)

Abstract of JP 2004140274 (A)

PROBLEM TO BE SOLVED: To manufacture a semiconductor device whose integration degree is high constituted of an MOS transistor(MOSFET (metal oxide semiconductor field effect transistor)) formed on an insulating layer by preventing etching from running through the insulating layer without stopping in a semiconductor layer at the time of forming a contact hole in an inter-layer insulating film. ; SOLUTION: An etching stopper film 12 having an etching speed slower than the etching speed of an inter-layer insulating film 8 is buried in the inter-layer insulating film 8, so that the side face of a silicon active layer 3 constituting an MOSFET element can be surrounded. ; COPYRIGHT: (C)2004,JPO



Data supplied from the *espacenet* database — Worldwide